Fig. 1A

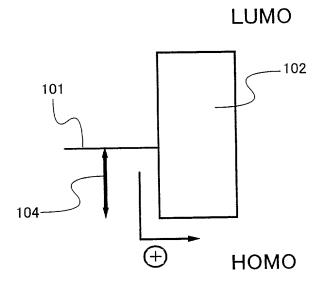


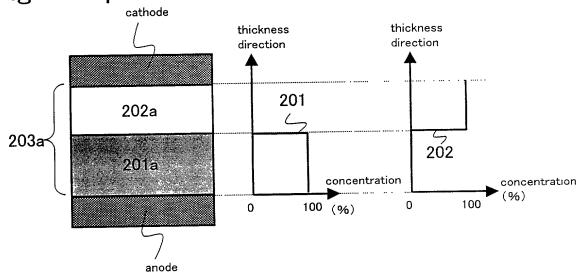
Fig. 1B LUMO

103

101

HOMO

Fig. 2A prior



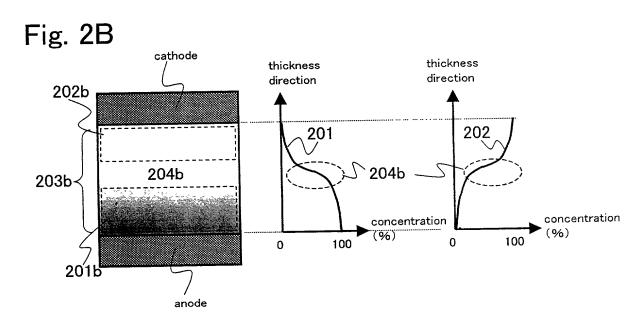


Fig. 3A

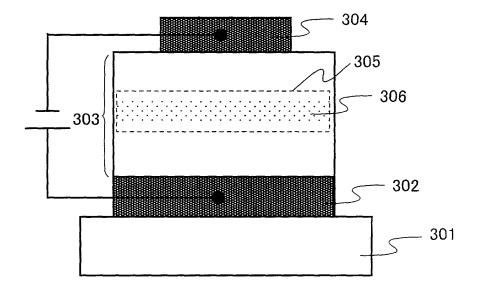
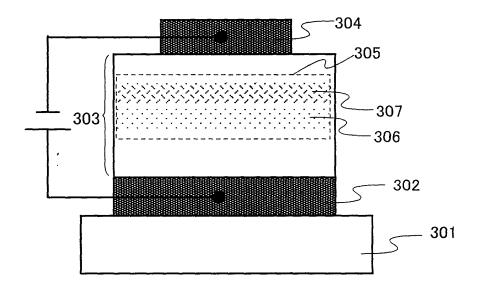
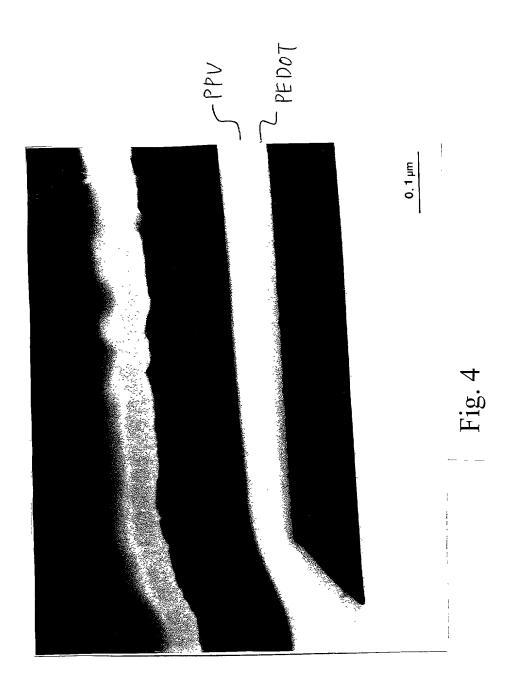
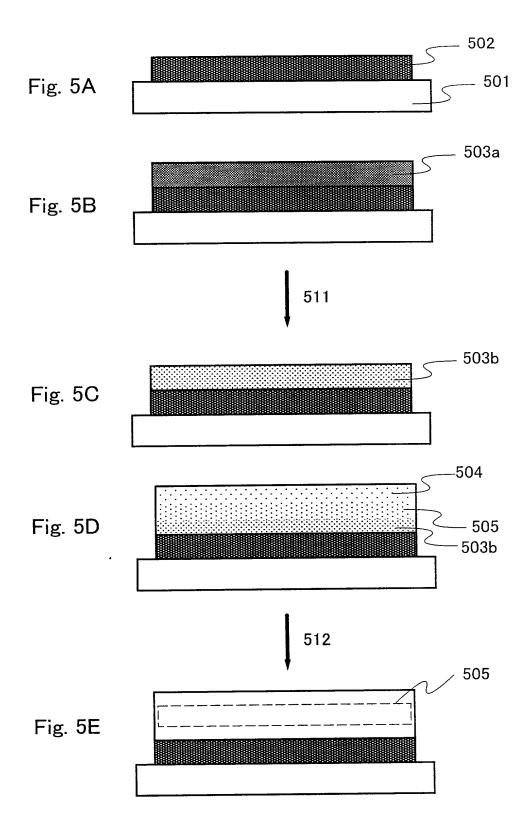
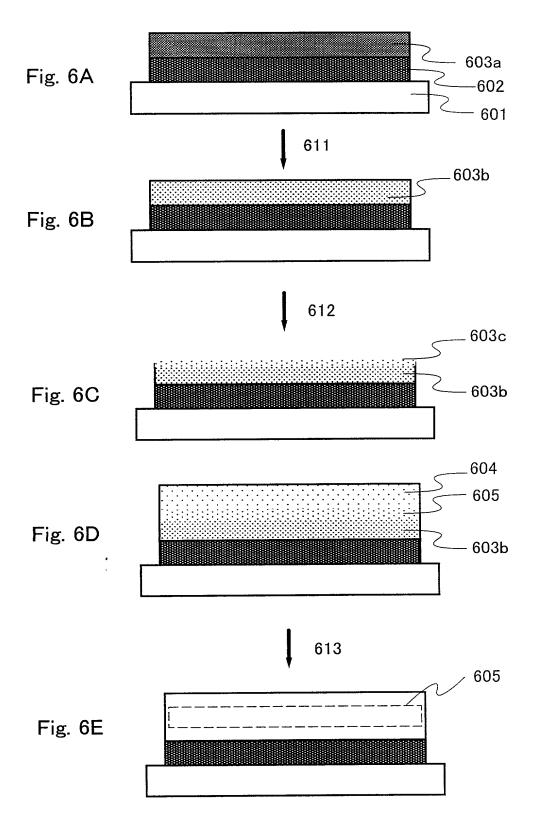


Fig. 3B









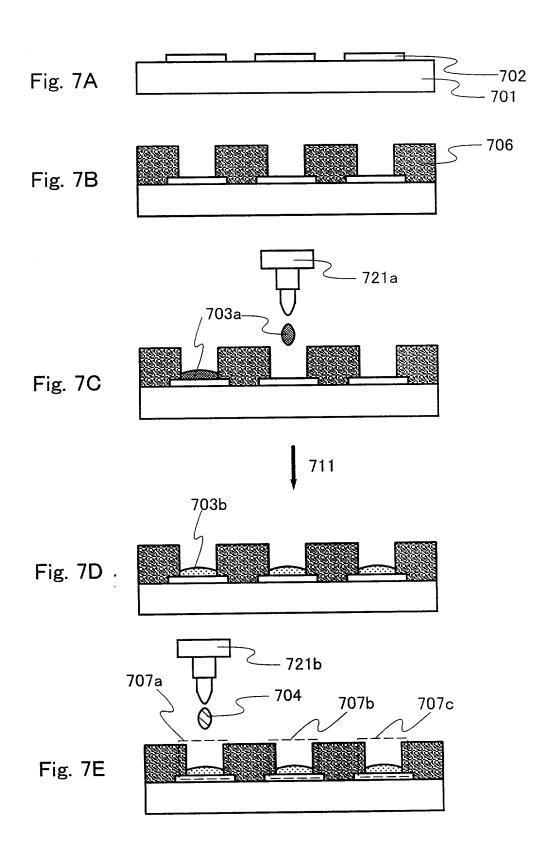
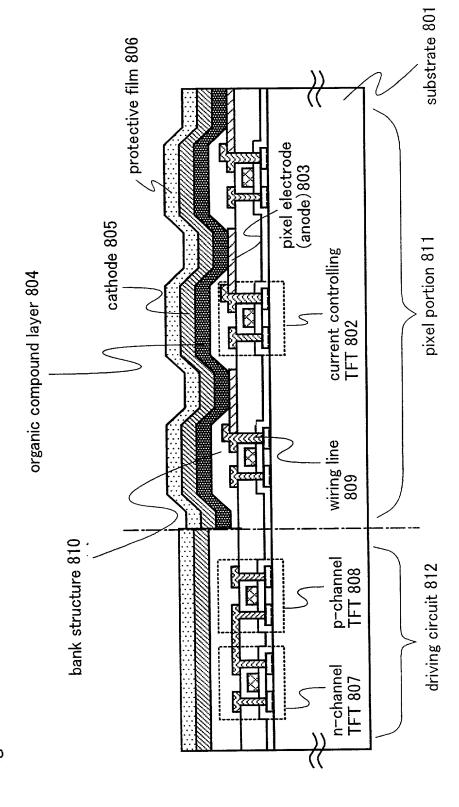
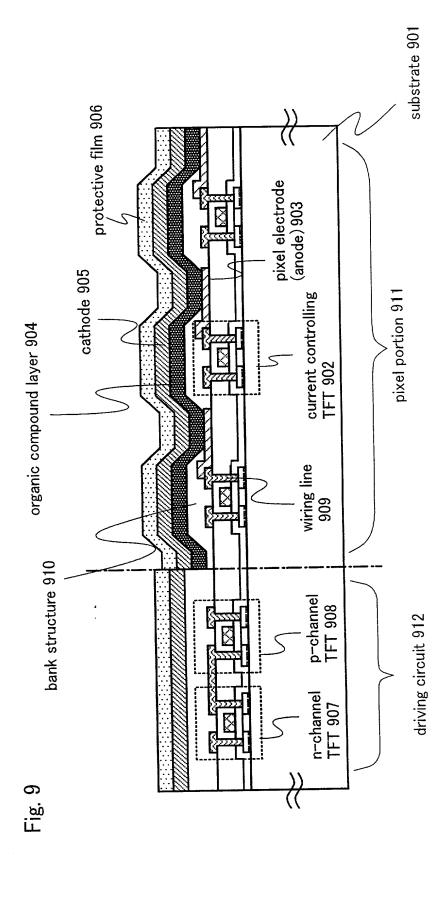
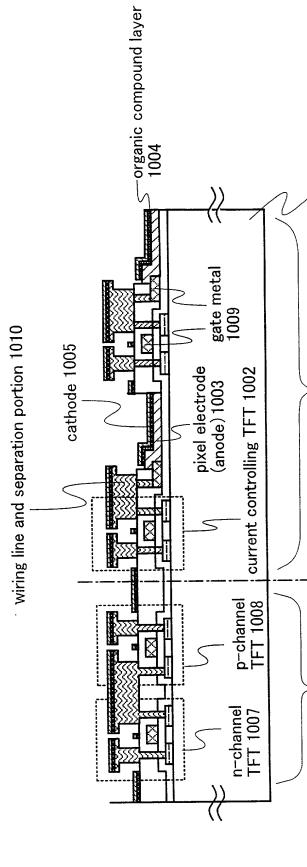


Fig. 8







substrate 1001

pixel portion 1011

driving circuit 1012

Fig. 10

Fig. 11A

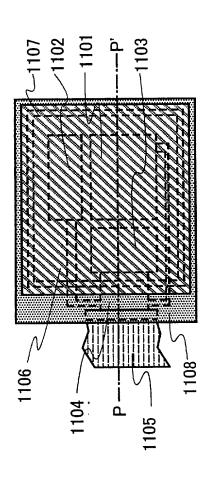


Fig. 11B

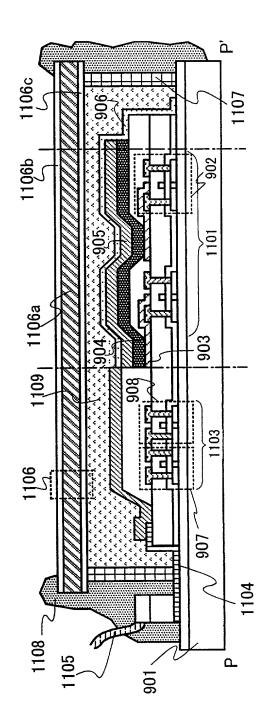


Fig. 12A

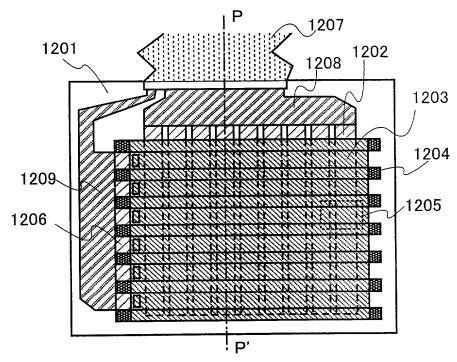


Fig. 12B

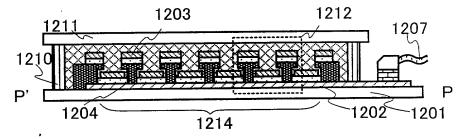
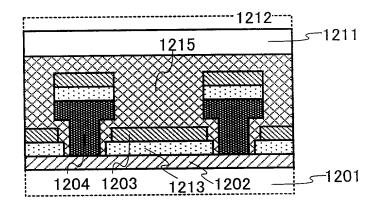


Fig. 12C





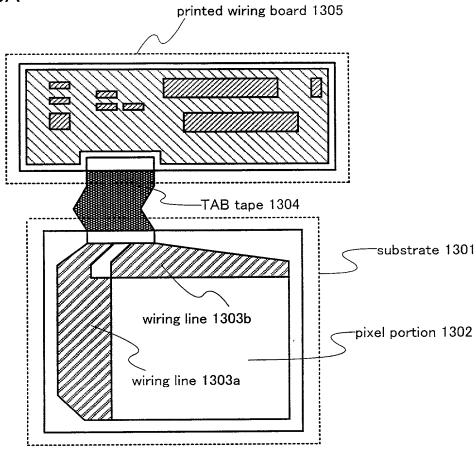


Fig. 13B

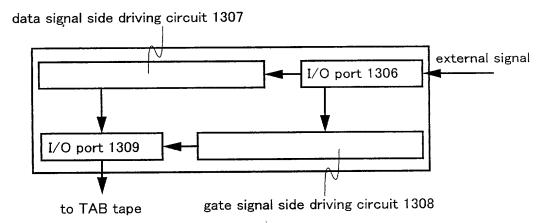


Fig. 14A

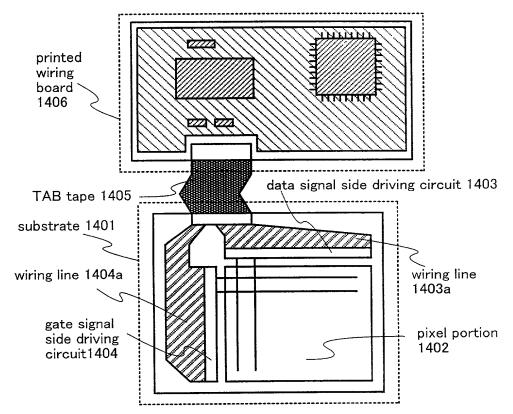


Fig. 14B

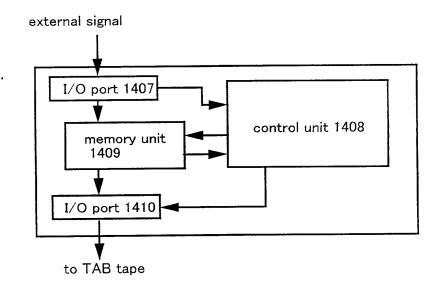


Fig. 15A

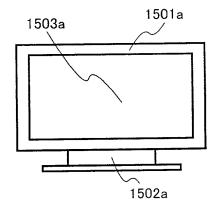


Fig. 15B 1506b

1502b

1505b

Fig. 15C

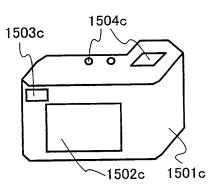
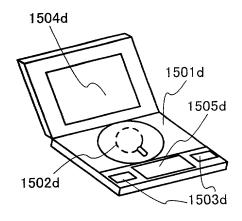


Fig. 15D



1503e 1502e 1505e

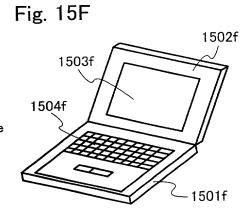
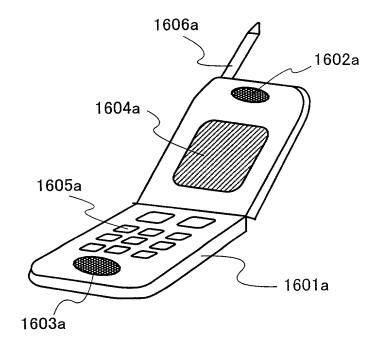
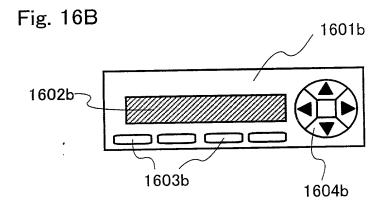
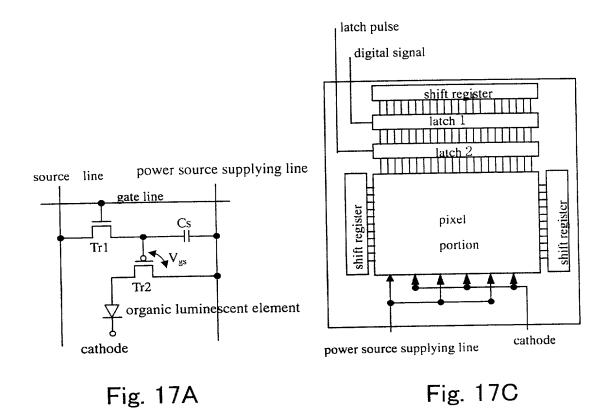
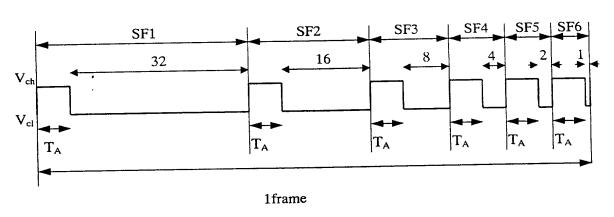


Fig. 16A









SF1-SF6:subframe, TA: write in time

Fig. 17B

Fig. 18

